Question Paper Code: 80453

B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2021.

Fifth Semester

Electronics and Instrumentation Engineering

EC 2312/10133 EE 503/EE 2354/10133 EC 506/EE 64 – MICROPROCESSORS AND MICROCONTROLLERS

(Common to Instrumentation and Control Engineering and Electrical and Electronics Engineering)

(Regulations 2008/2010)

(Also common to PTEE 2354/PTEC 2312/10133 EE 503 – Microprocessors and Microcontroller for B.E. (Part-Time) Fourth Semester – Electrical and Electronics Engineering – Regulations 2009/2010)

Time: Three hours Maximum: 100 marks

Answer ALL questions.

PART A — $(10 \times 2 = 20 \text{ marks})$

- 1. Specify the processor size of 8085 and 8086 microprocessors.
- 2. State the function of TF and DF flags in 8086 microprocessor.
- 3. State the function of 8085 instructions: CPI and RRC.
- 4. Write an assembly language program to place the contents of two consecutive memory locations to registers B and C respectively.
- 5. What are the functions performed by INTEL 8251A?
- 6. What is scanning in keyboard and what is scan time?
- 7. Mention the advantages of Harvard Architecture
- 8. Give the flags available in 8051.
- 9. What is the application of bit wise instruction?
- 10. What is PCON register?

PART B — $(5 \times 16 = 80 \text{ marks})$

11.	(a)	Expl	ain the architecture of 8086 microprocessor in detail.	(16)
			Or	
	(b)	Draw the functional diagram of 8085 and explain it in detail.		
12.	(a)	(i)	With suitable examples, explain the program control instruction 8085 microprocessor.	ns of (8)
		(ii)	Write an 8085 based assembly language program to compute sum of 'n' elements.	the (8)
			Or	
	(b)	(i)	Explain the data manipulation instructions of 8086 microproces	sor. (8)
		(ii)	Write an 8086 based assembly language program to count number of occurrences of a given data in a set of numbers.	the (8)
13.	(a)	(i)	What is the need to interfacing PPI (8255) in 8085?	(4)
		(ii)	Draw and explain the architecture of 8259.	(12)
			Or	
	(b)	(i)	What are the important steps involved to interface an multicha ADC with 8085?	nnel (4)
		(ii)	Draw and explain the architecture of 8253 timer.	(12)
14.	(a)	Discuss about the memory organization and special function registe 8051 Microcontroller.		rs in (16)
			Or	
	(b)	(i)	Explain the function and operating modes with the associ registers of Timer/Counter in 8051 Microcontroller.	ated (10)
		(ii)	Discuss about the interrupts of 8051 Microcontroller.	(6)
15.	(a)		n suitable diagrams, explain the closed loop control of servom microprocessors.	otor
			Or	
	(b)	_	ain the role of micro controller in washing machine control mbly language program.	with

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